Code :R7310402

R7

III B.Tech I Semester(R07) Supplementary Examinations, May 2011
COMPUTER ORGANIZATION
(Common to Electronics & Communication Engineering, Electronics & Instrumentation
Engineering, Electronics & Control Engineering)

Time: 3 hours   Max Marks: 80

Answer any FIVE questions
All questions carry equal marks
⋆ ⋆ ⋆ ⋆ ⋆

1. Explain about:

    (a) Primary and secondary storages

    (b) Random access memory

2. (a) Explain the three-state bus buffer with a suitable example.

    (b) Explain about memory transfer with an example.

3. What are the differences hardwired control and micro programmed control.

4. What are the advantages of Serial-Adder and Parallel-Adder?

5. (a) Differentiate between Static RAM and Dynamic RAM.

    (b) Give a detailed note on organization of Cache Memory.

6. (a) What is the difference between I/O program-controlled transfer and DMA transfer?

    (b) Explain how processor responds to an interrupt.

7. (a) What is pipelining? Demonstrate the pipeline organization with an example.

    (b) Explain how matrix multiplication is performed with vector processors?

8. (a) What is multiprocessing? Explain its benefits.

    (b) Explain serial arbitration and parallel arbitration procedures in detail.

⋆ ⋆ ⋆ ⋆ ⋆